

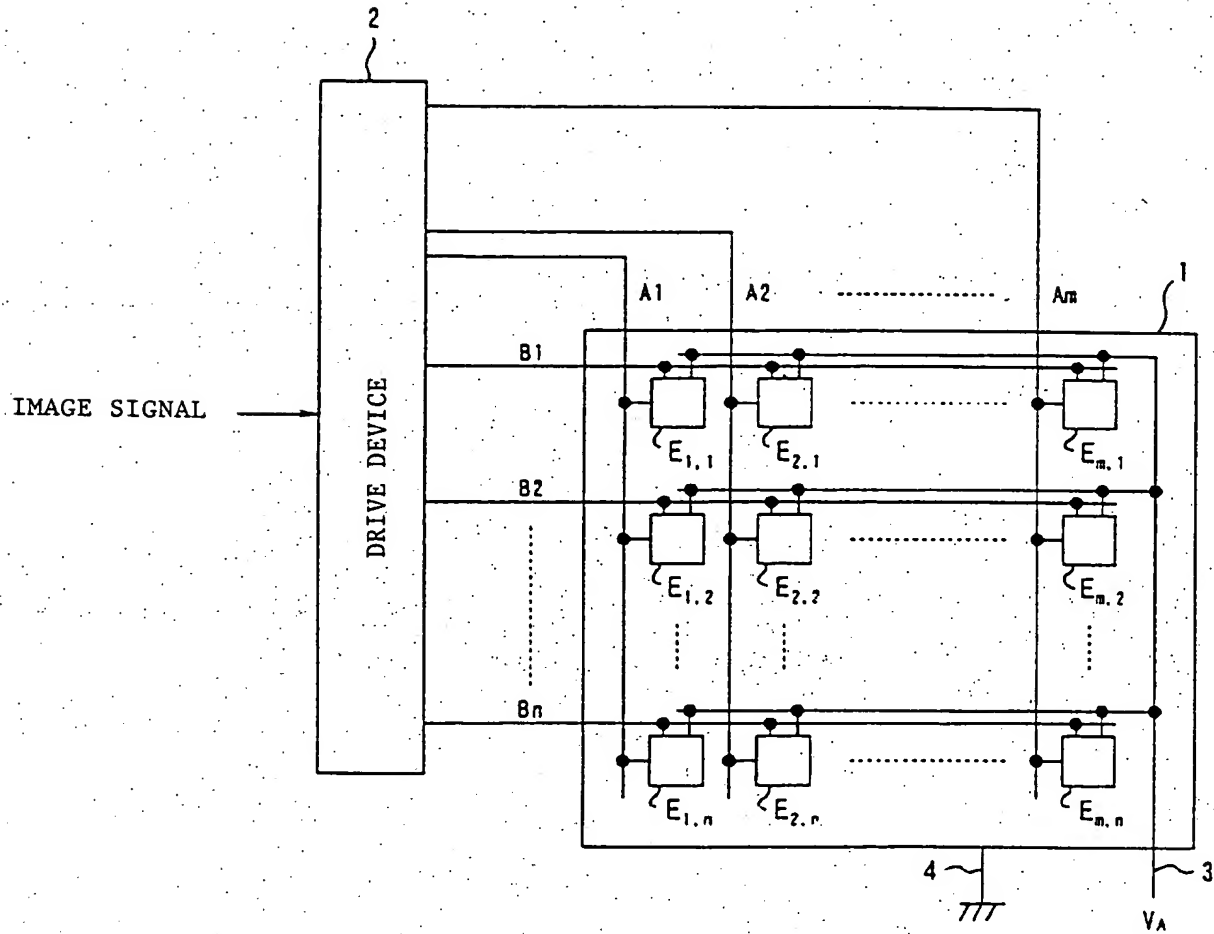
**FIG. 1**

FIG. 2

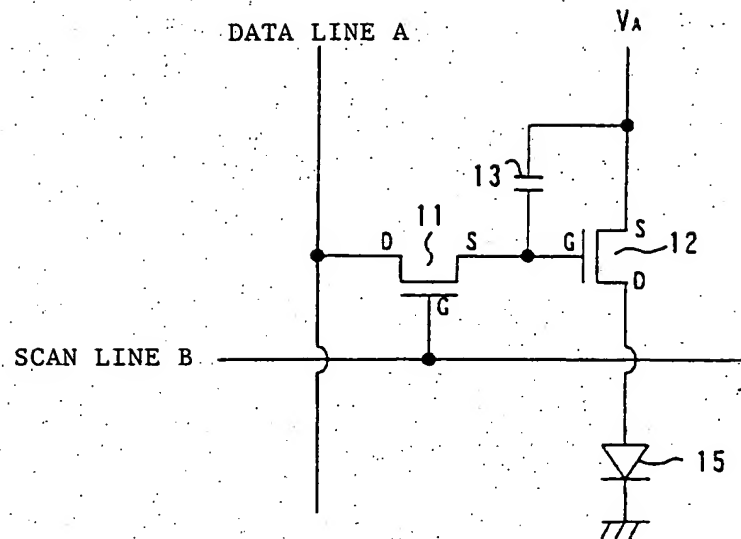
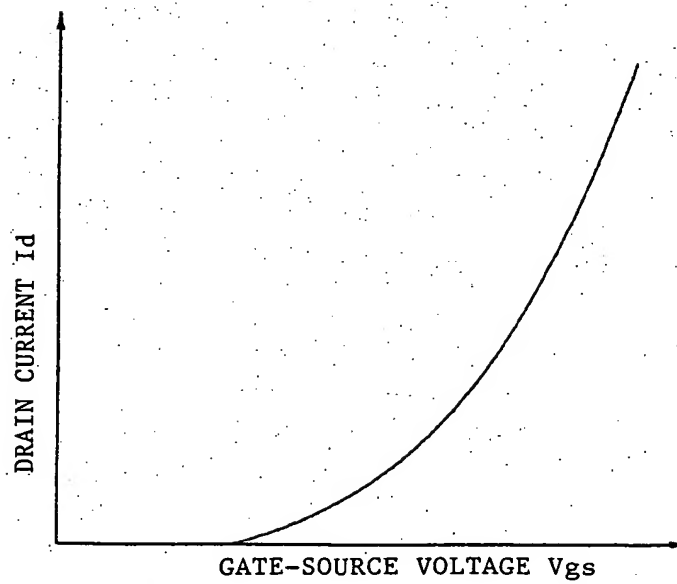


FIG. 3



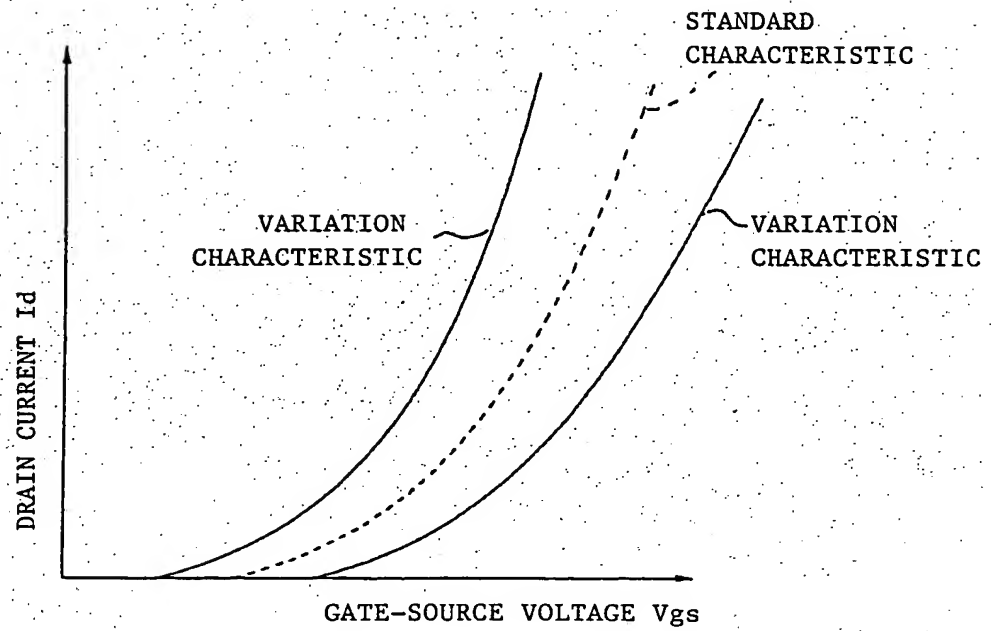
**FIG. 4**

FIG. 5A

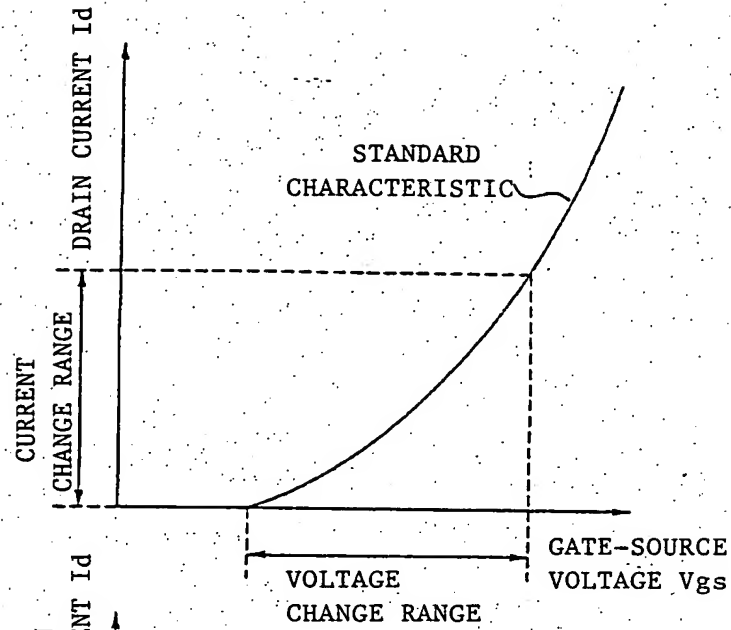


FIG. 5B

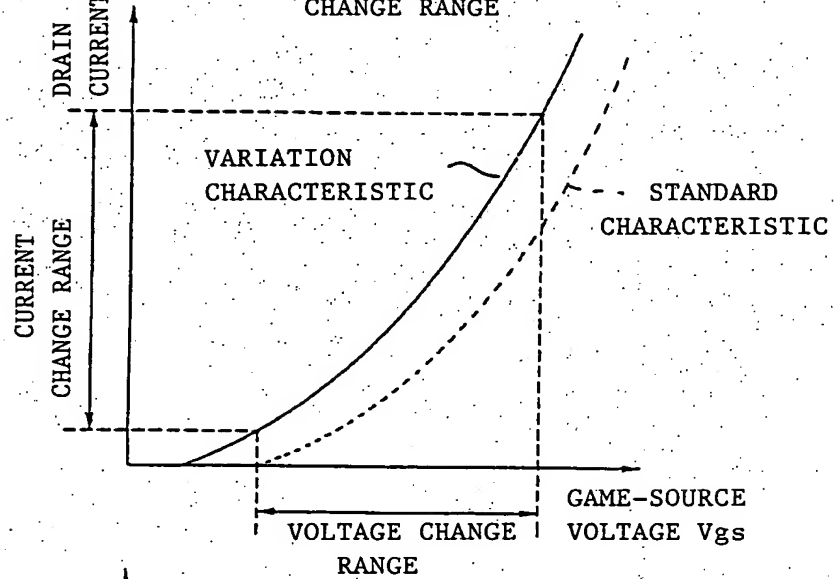


FIG. 5C

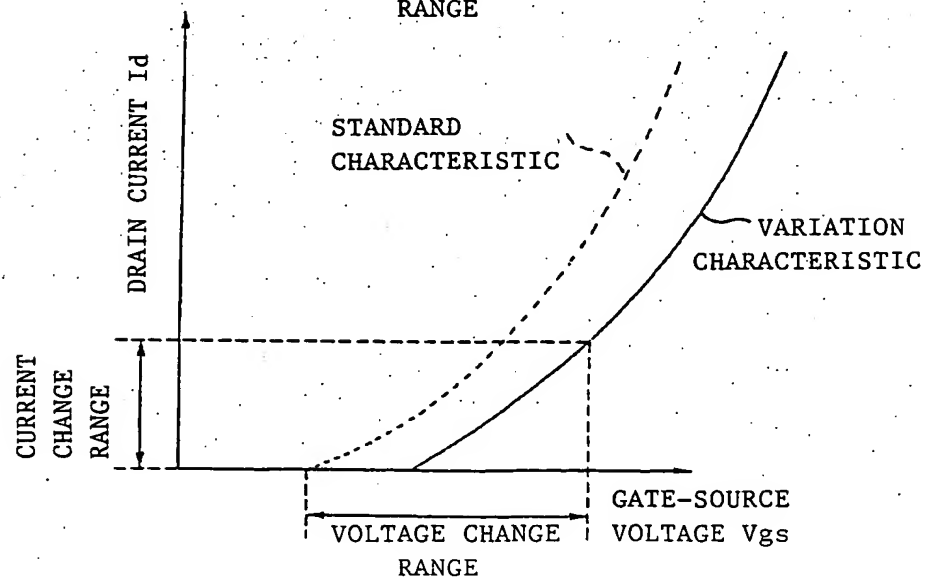


FIG. 6

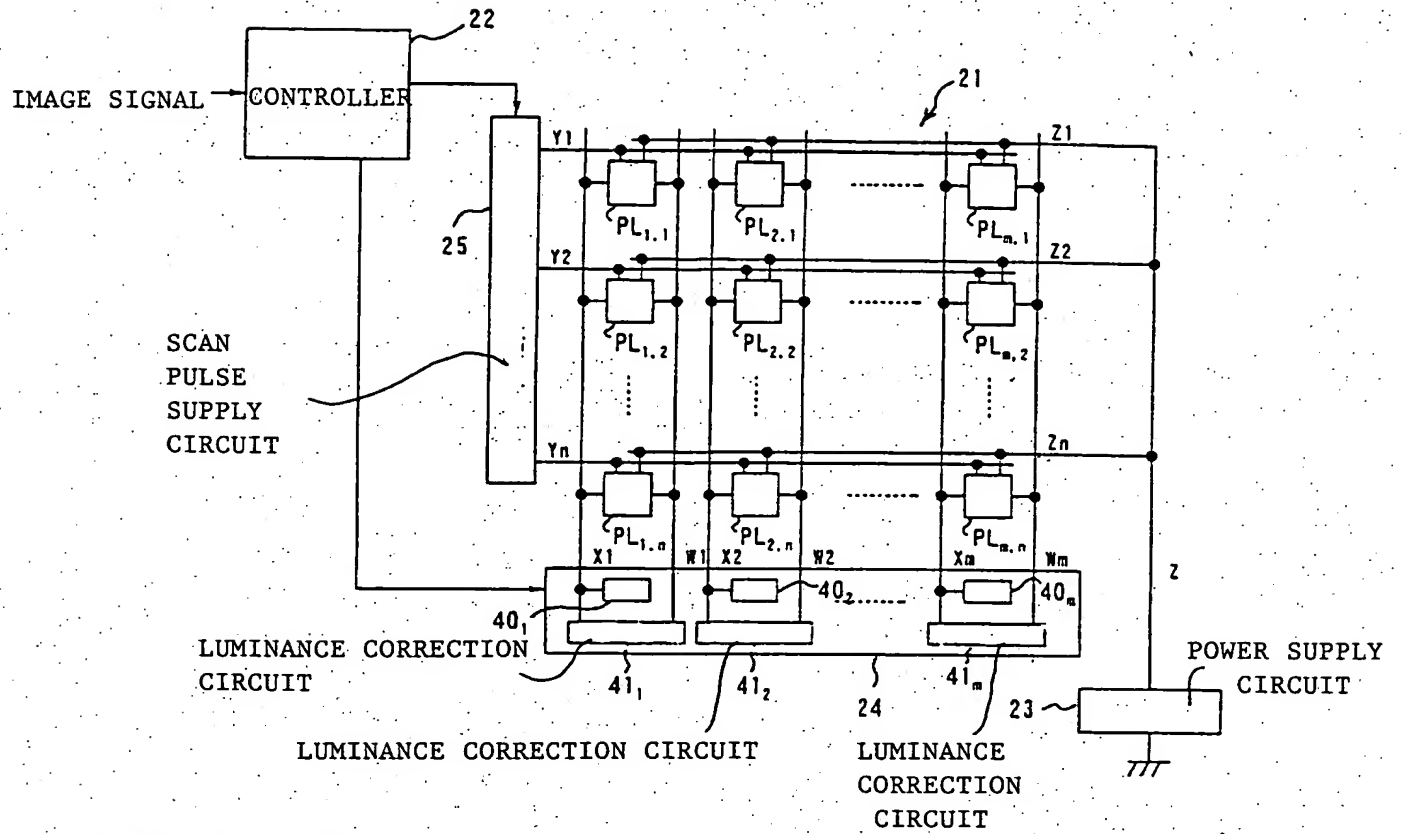
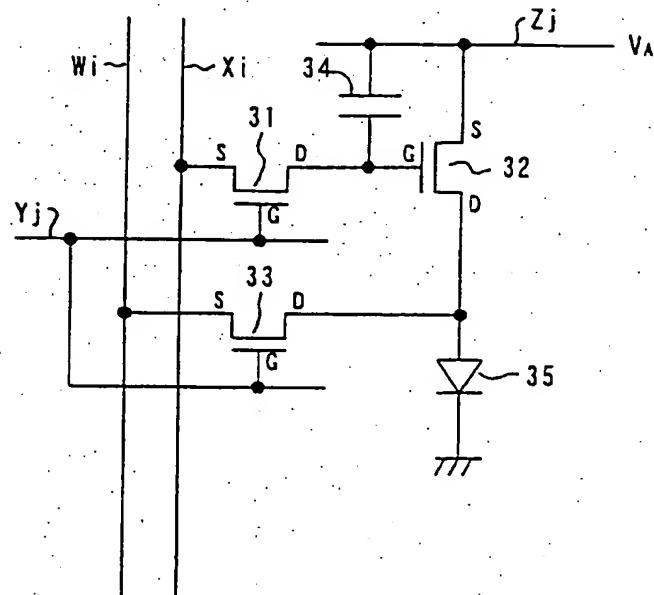
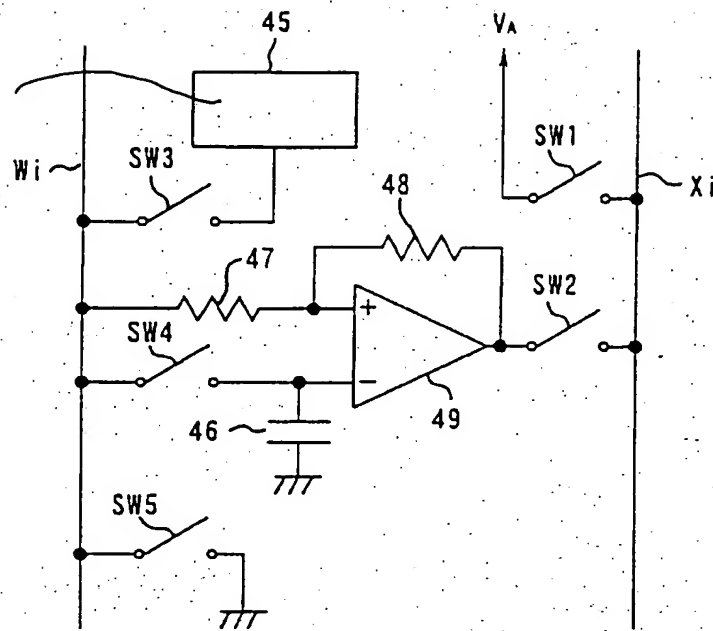


FIG. 7



**FIG. 8**

CURRENT  
GENERATION  
CIRCUIT



## FIG. 9

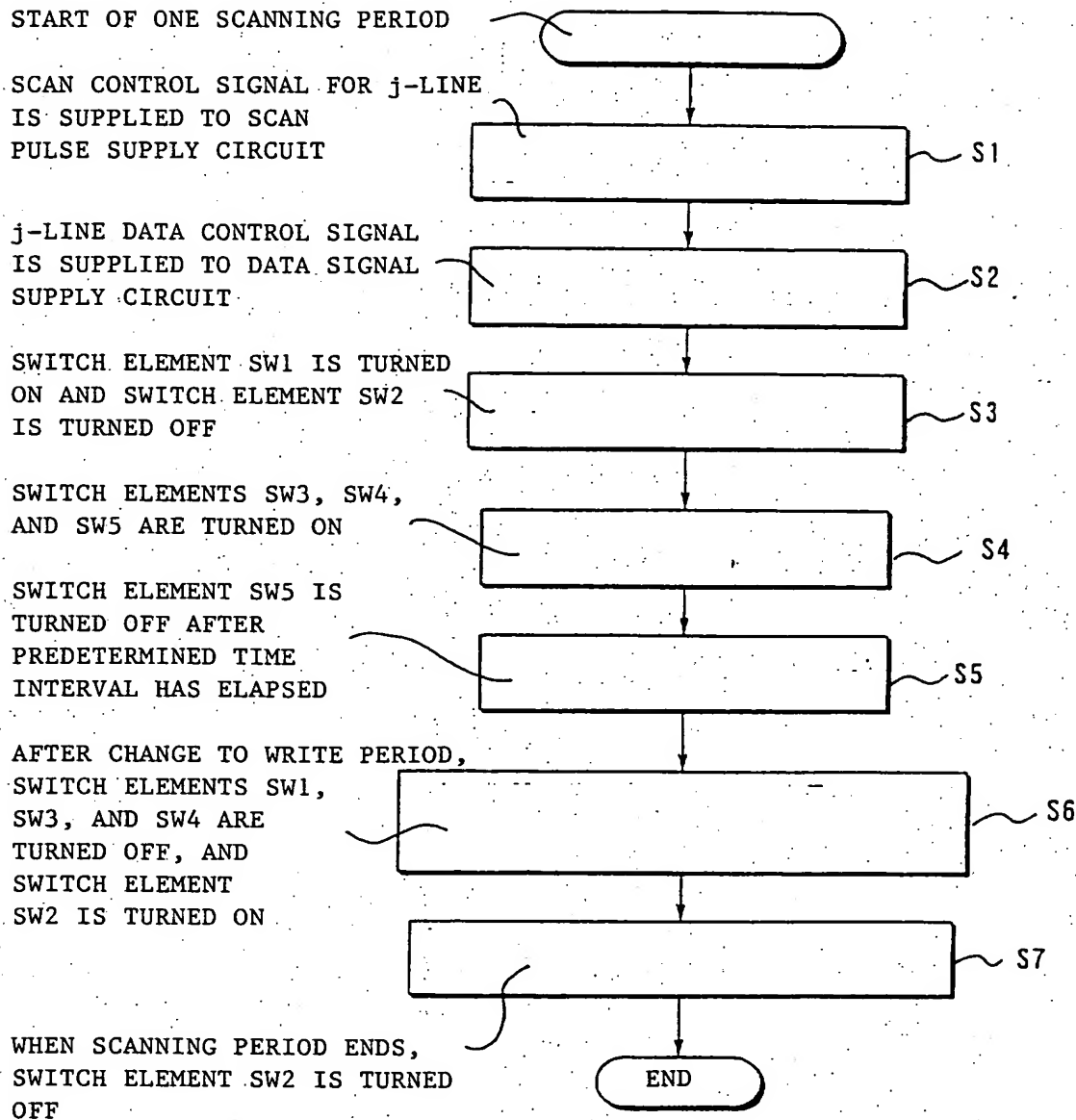


FIG. 10

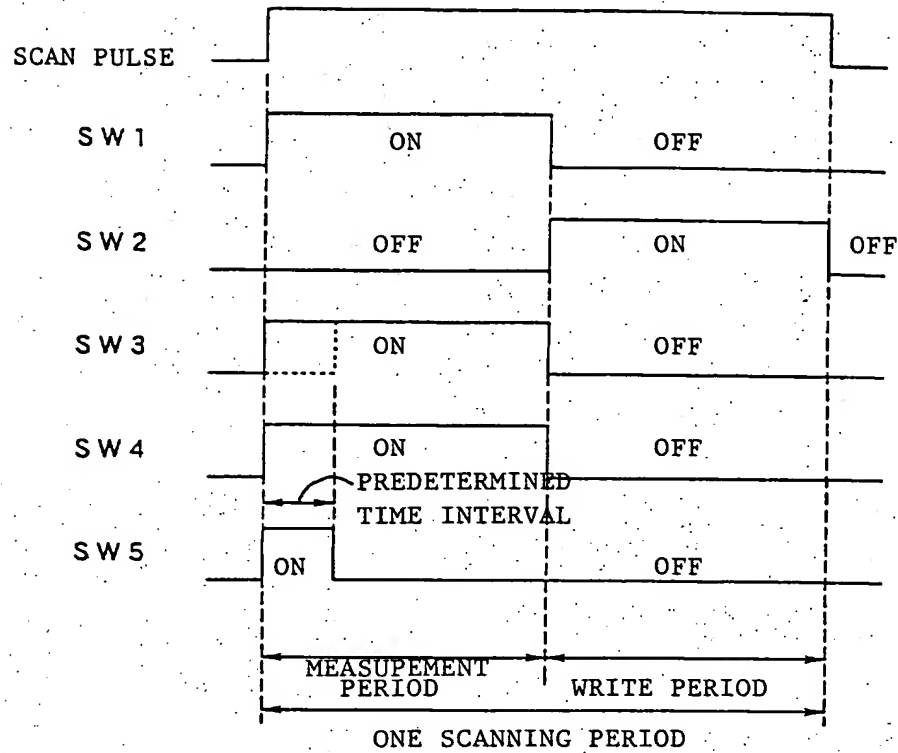


FIG. 11

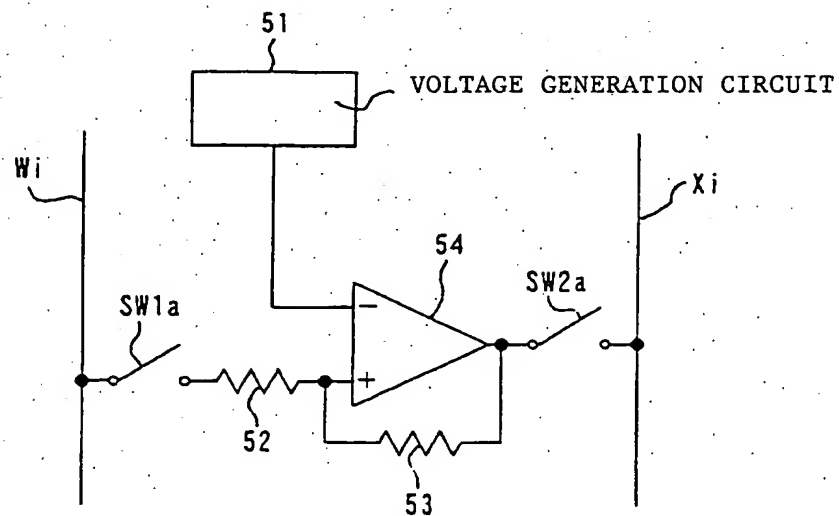




FIG. 12

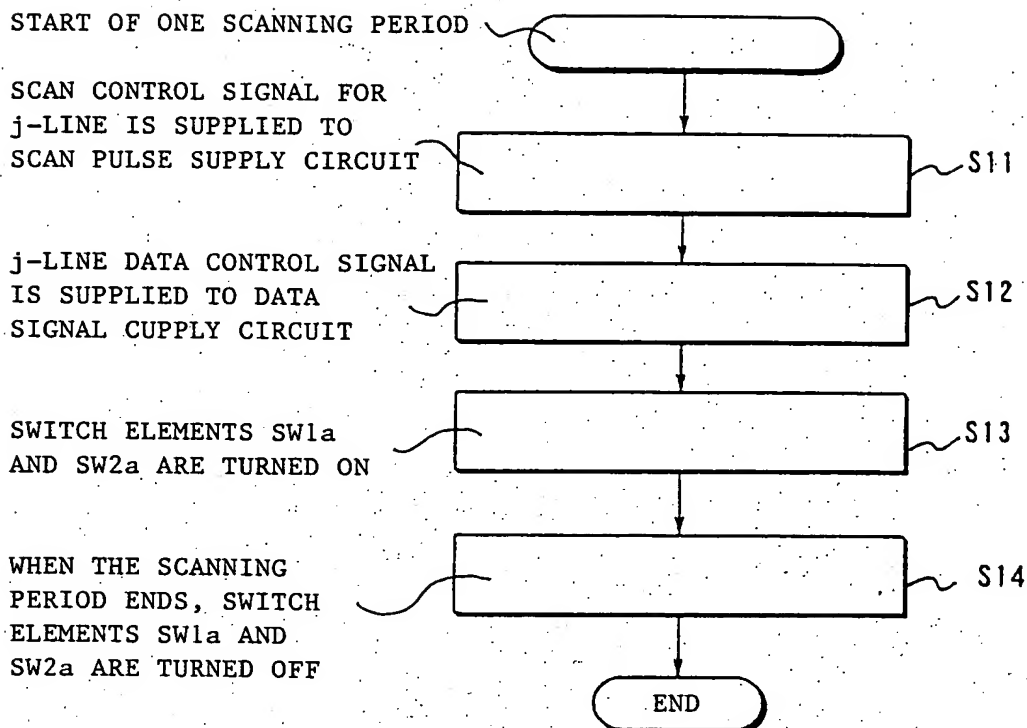


FIG. 13

